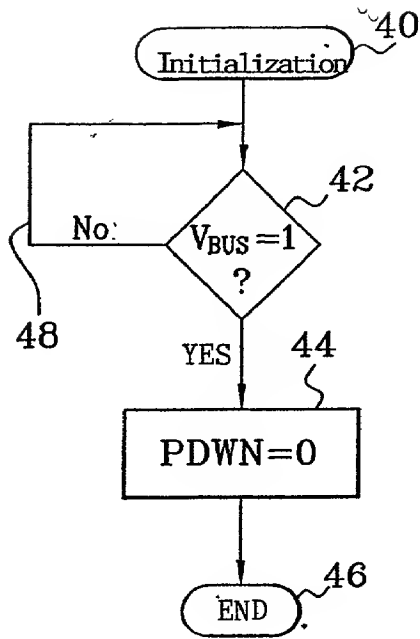
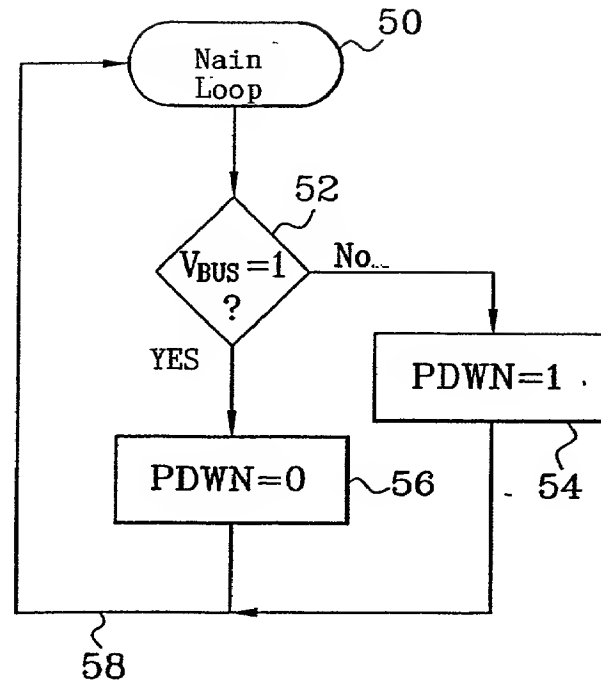
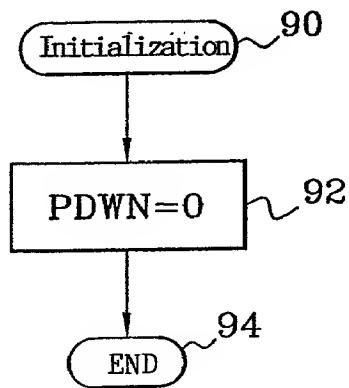
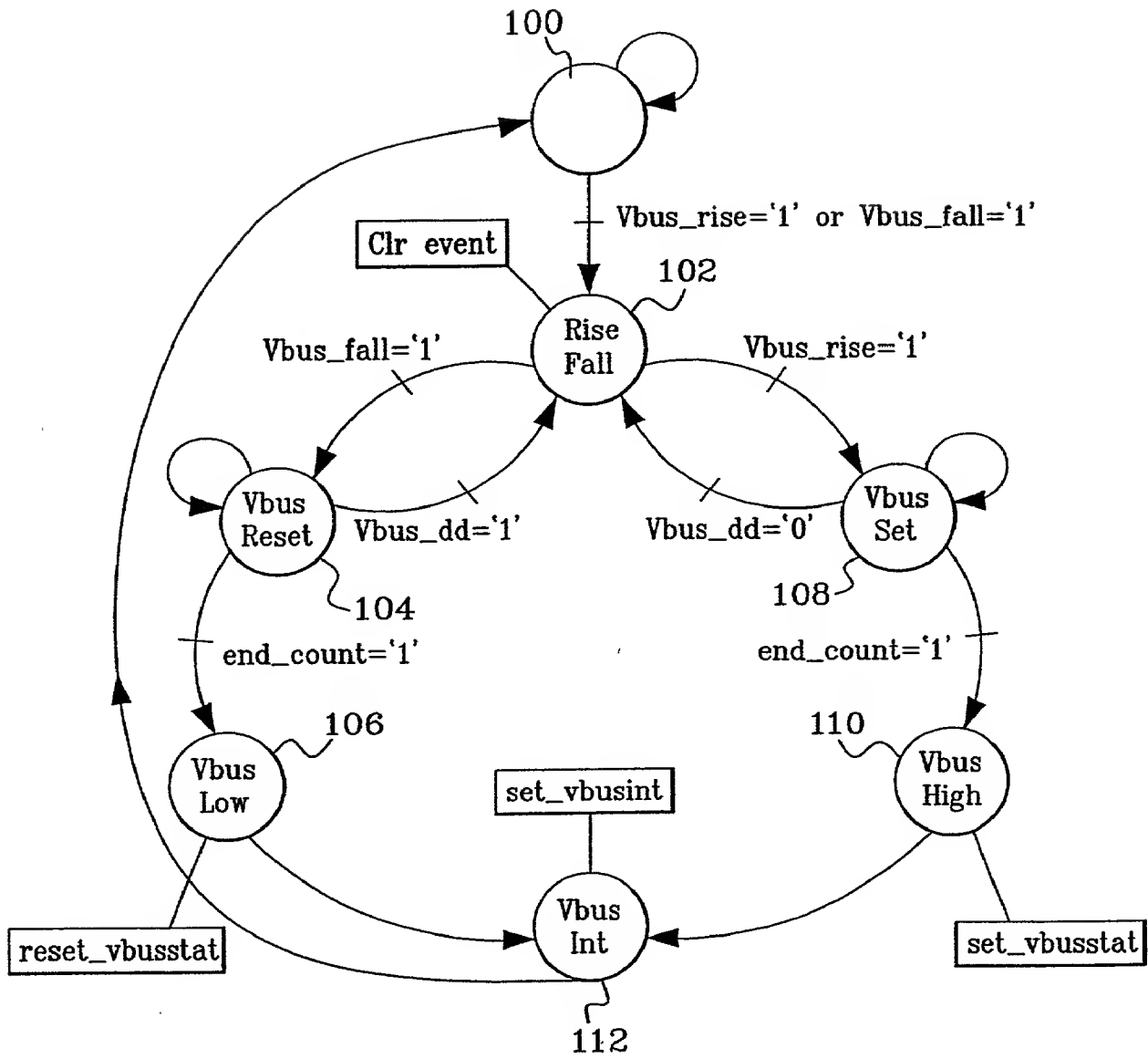


Fig. 1

**Fig. 2****Fig. 3****Fig. 4**

VBUSSTAT	PDWN	State of Regulator 26
0	0	Arrêt
0	1	Arrêt
1	0	Marche
1	1	Arrêt

**Fig. 5**

Fig. 8